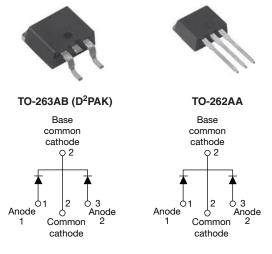


Vishay Semiconductors

Ultrafast Rectifier, 2 x 10 A FRED Pt[®]



VS-MURB2020CTPbF

VS-MURB2020CT-1PbF

PRODUCT SUMMARY	
Package	TO-263AB (D ² PAK), TO-262AA
I _{F(AV)}	2 x 10 A
V _R	200 V
V _F at I _F	0.85 V
t _{rr}	35 ns
T _J max.	175 °C
Diode variation	Common cathode

Revision: 10-Jul-15

FEATURES

- · Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM R	ATINGS				
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage		V _{RRM}		200	V
Average rectified forward currentper I	per leg	1		10	
Average rectilied forward current	total device	I _{F(AV)}	Rated V _R , T _C = 145 °C	20	۸
Non-repetitive peak surge current p	er leg	I _{FSM}		100	A
Peak repetitive forward current per leg		I _{FM}	Rated V_R , square wave, 20 kHz, T_C = 145 °C	20	
Operating junction and storage tem	peratures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	
		I _F = 8 A, T _J = 125 °C	-	-	0.85	V
Forward voltage	V _F	I _F = 16 A	-	-	1.15	
		I _F = 16 A, T _J = 125 °C	-	-	1.05	
Reverse leakage current		$V_{R} = V_{R}$ rated	-	-	15	
neverse leakage current	ent I_R $T_J = 150 °C,$	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA
Junction capacitance	CT	V _R = 200 V	-	55	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH



For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000

1



www.vishay.com

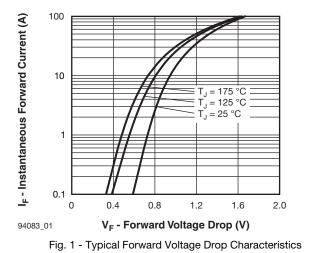
Vishay Semiconductors

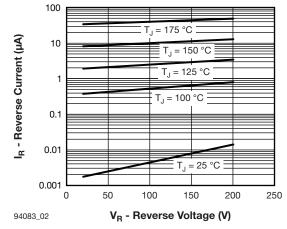
DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	50 A/µs, V _R = 30 V	-	-	35	
Reverse recovery time	+	I _F = 1.0 A, dI _F /dt =	100 A/µs, V _R = 30 V	-	19	-	ns
neverse recovery lime	t _{rr}	T _J = 25 °C		-	21	-	
		T _J = 125 °C		-	35	-	
Dook roooyony ourront	1	T _J = 25 °C	$I_{\rm F} = 10 {\rm A}$	-	1.9	-	А
Peak recovery current	I _{RRM}	T _J = 125 °C	dl _F /dt = 200 A/µs V _R = 160 V	-	4.8	-	~
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	25	-	nC
		T _J = 125 °C		-	78	-	

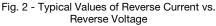
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	2.5	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Waight			-	2.0	-	g
Weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking davias		Case style TO-263AB (D ² PAK)		MURB	2020CT	
Marking device		Case style TO-262AA		MURB2	020CT-1	

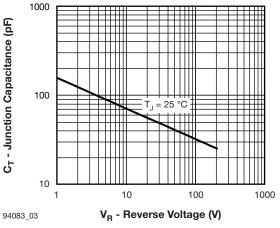


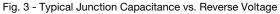
Vishay Semiconductors











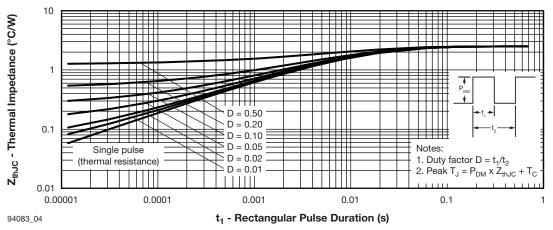


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

Revision: 10-Jul-15

3

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



Vishay Semiconductors

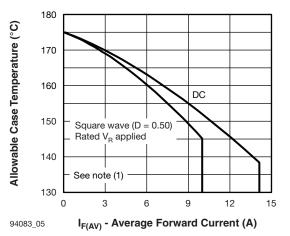


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

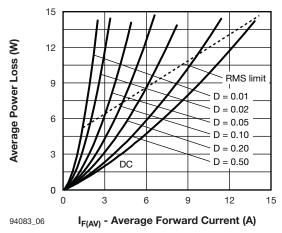
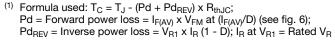


Fig. 6 - Forward Power Loss Characteristics

Note



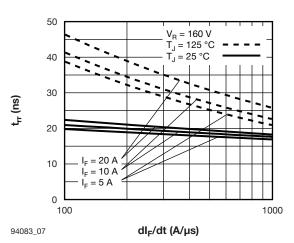


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

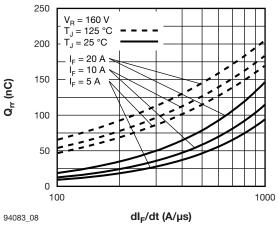


Fig. 8 - Typical Stored Charge vs. dl_F/dt



Vishay Semiconductors

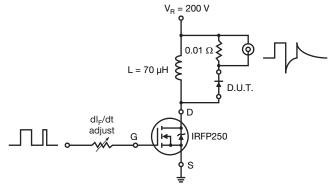


Fig. 9 - Reverse Recovery Parameter Test Circuit

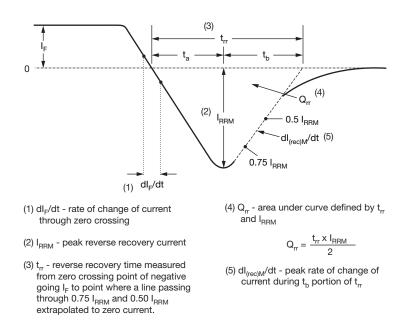


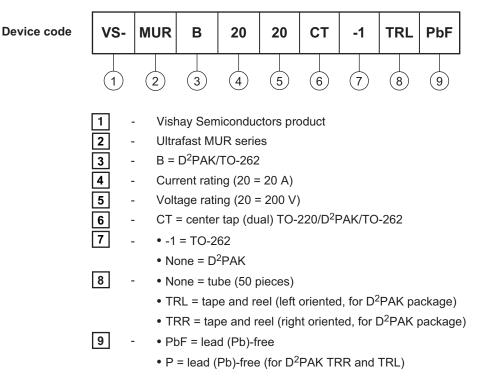
Fig. 10 - Reverse Recovery Waveform and Definitions

www.vishay.com

VS-MURB2020CTPbF, VS-MURB2020CT-1PbF

Vishay Semiconductors

ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95014				
Part marking information	www.vishay.com/doc?95008				
Packaging information	www.vishay.com/doc?95032				
SPICE model	www.vishay.com/doc?95622				

 Revision: 10-Jul-15
 6
 Document Number: 94083

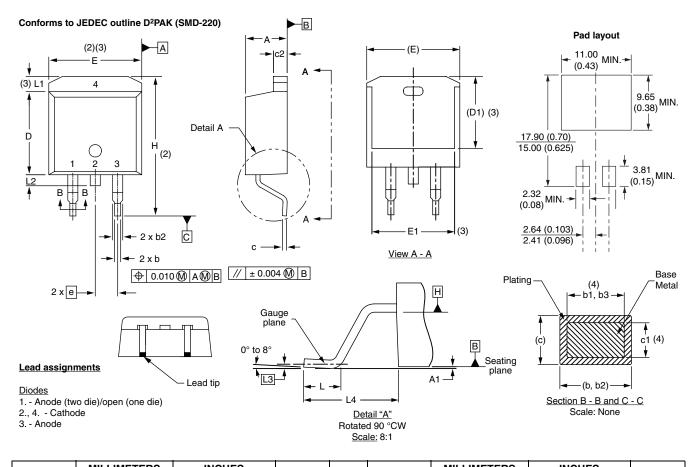
 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000

Vishay High Power Products

D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches

SHA



SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

⁽⁷⁾ Outline conforms to JEDEC outline TO-263AB

Notes

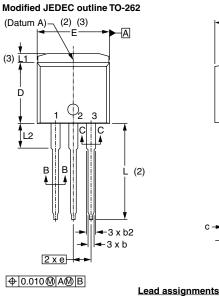
- ⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- ⁽⁴⁾ Dimension b1 and c1 apply to base metal only
- ⁽⁵⁾ Datum A and B to be determined at datum plane H
- ⁽⁶⁾ Controlling dimension: inch

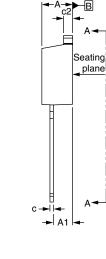
Vishay High Power Products

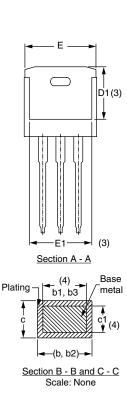
D²PAK, TO-262



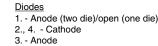
DIMENSIONS FOR TO-262 in millimeters and inches







Lead tip



OVMDOL	MILLIM	IETERS	INCHES		NOTEO
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100	BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.56	3.71	0.140	0.146	

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- ⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Controlling dimension: inches

⁽⁶⁾ Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

www.vishay.com 2



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.