

# SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

# ATP208 — General-Purpose Switching Device Applications

#### **Features**

- · Low ON-resistance
- 4.5V drive
- · Halogen free compliance

- · Large current
- · Slim package
- · Protection diode in

# **Specifications**

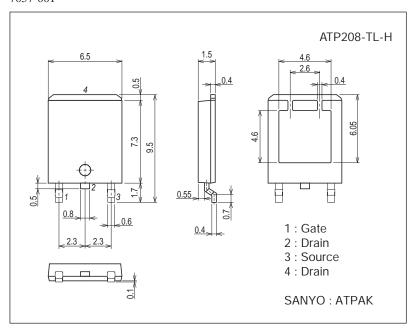
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		40	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		90	А
Drain Current (PW≤10μs)	IDP	PW≤10μs, duty cycle≤1%	270	А
Allowable Power Dissipation	PD	Tc=25°C	60	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *1	EAS		155	mJ
Avalanche Current *2	IAV		45	А

Note :\*1 VDD=15V, L=100 $\mu$ H, IAV=45A

#### **Package Dimensions**

unit : mm (typ) 7057-001



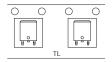
## **Product & Package Information**

• Package : ATPAK

• JEITA, JEDEC :-

• Minimum Packing Quantity : 3,000 pcs./reel

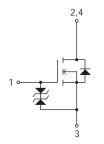
#### Packing Type: TL



#### Marking



#### **Electrical Connection**



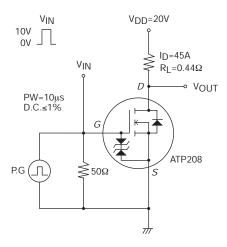
<sup>\*2</sup> L≤100µH, Single pulse

# **ATP208**

# Electrical Characteristics at Ta=25°C

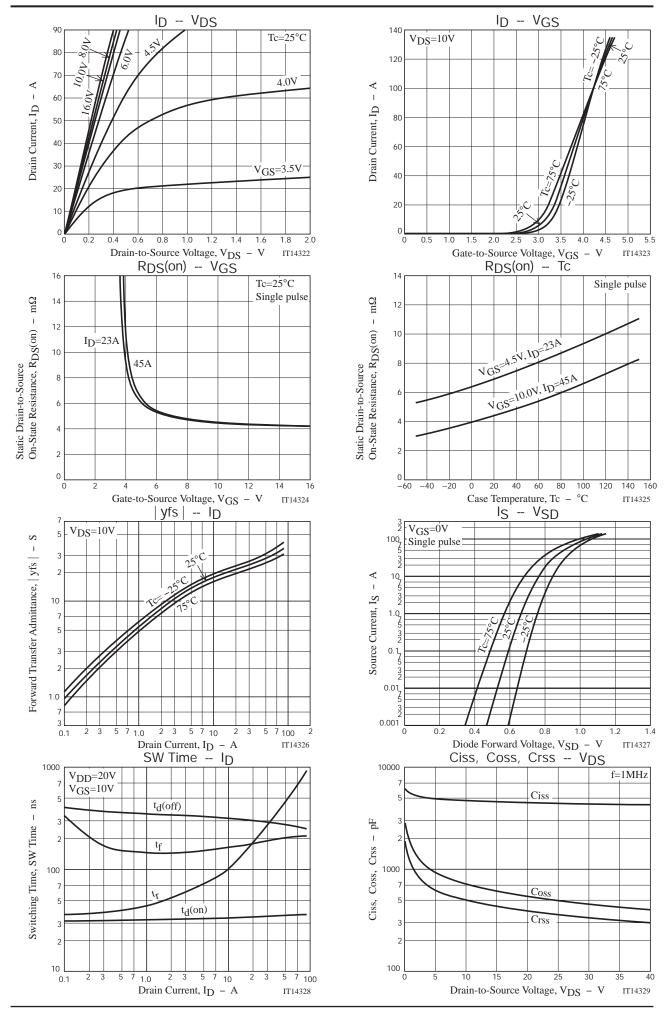
Parameter	Cumbal	Conditions	Ratings			Unit	
Parameter	Symbol	Conditions	min	typ	max	Offic	
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	40			V	
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	μΑ	
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μΑ	
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.5		2.6	٧	
Forward Transfer Admittance	yfs	VDS=10V, ID=45A	16	28		S	
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =45A, V <sub>G</sub> S=10V		4.6	6.0	mΩ	
	R <sub>DS</sub> (on)2	I <sub>D</sub> =23A, V <sub>G</sub> S=4.5V		7	9.8	mΩ	
Input Capacitance	Ciss			4510		pF	
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		535		pF	
Reverse Transfer Capacitance	Crss			385		pF	
Turn-ON Delay Time	t <sub>d</sub> (on)			35		ns	
Rise Time	t <sub>r</sub>	Sac appointed Toot Circuit		400		ns	
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		280		ns	
Fall Time	tf			200		ns	
Total Gate Charge	Qg			83		nC	
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =20V, V <sub>GS</sub> =10V, I <sub>D</sub> =90A		19		nC	
Gate-to-Drain "Miller" Charge	Qgd			17		nC	
Diode Forward Voltage	V <sub>SD</sub>	IS=90A, VGS=0V		1.0	1.2	V	

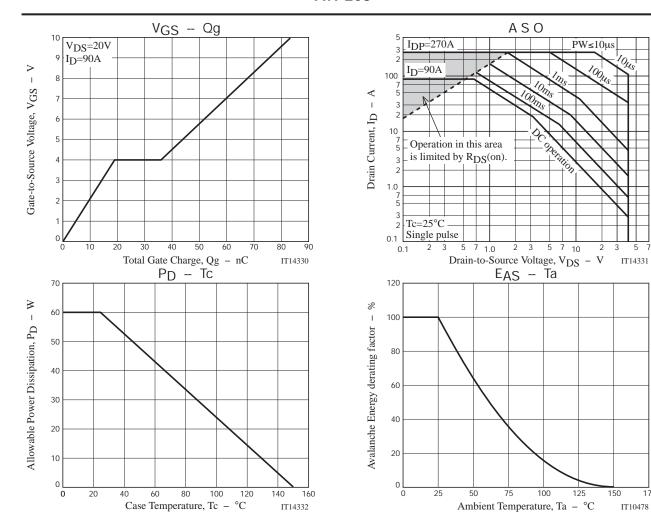
# Switching Time Test Circuit



# **Ordering Information**

Device	Device Package		memo	
ATP208-TL-H	ATPAK	3,000pcs./reel	Pb Free and Halogen Free	





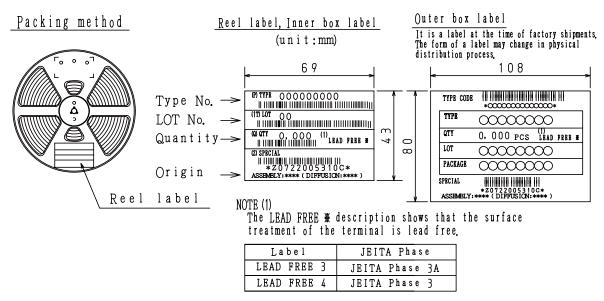
175

#### **Taping Specification**

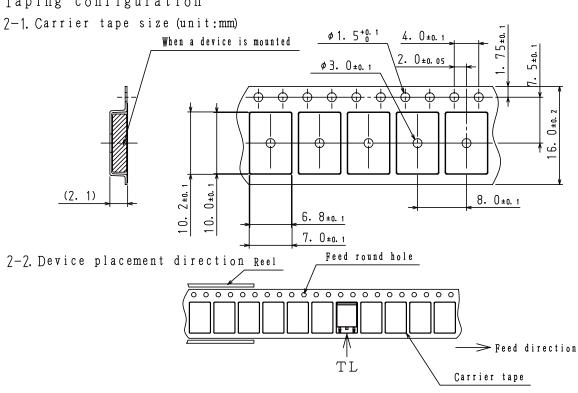
ATP208-TL-H

## 1. Packing Format (TL)

Package Name Carrier		Maximum Number of devices contained (pcs)			Packing format		
rackage Name	Туре	Reel	Inner box	Outer box	INNER BOX SD-C-18	OUTER BOX SD-A-18	
					1 reels contained	5 inner boxes contained	
ATPAK	ATP	3,000	3,000 15.	15,000	Dimensions:mm (external)	Dimensions:mm (external)	
					340×340×28	355×355×165	



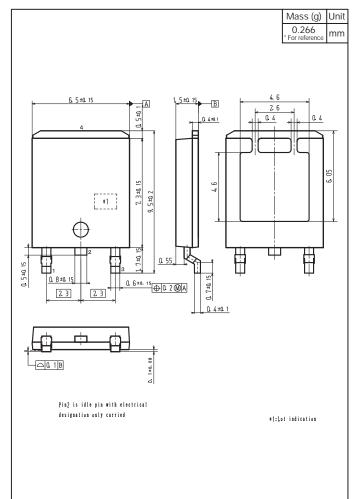
#### 7. Taping configuration



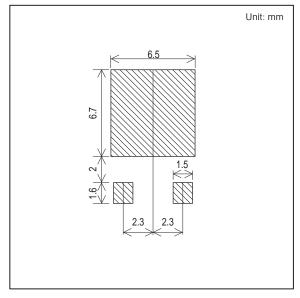
The one erectrode terminals on feed hole side····TL

# **Outline Drawing**

ATP208-TL-H



# **Land Pattern Example**



Note on usage: Since the ATP208 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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