MSRD620CTG, MSRD620CTT4G, SSRD8620CTT4G

SWITCHMODE Soft Ultrafast Recovery Power Rectifier

Plastic DPAK Package

State-of-the-art geometry features epitaxial construction with glass passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies, free wheeling diode and polarity protection diodes.

Features

- Soft Ultrafast Recovery (35 ns typ)
- Highly Stable Oxide Passivated Junction
- Matched Dual Die Construction May Be Paralleled for High Current Output
- Short Heat Sink Tab Manufactured Not Sheared
- Epoxy Meets UL 94 V-0 @ 0.125 in.
- AEC-Q101 Qualified and PPAP Capable
- SSRD8 Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free and are RoHS Compliant*

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 0.4 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- ESD Ratings:
 - ♦ Machine Model = C
 - ◆ Human Body Model = 2



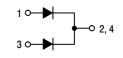
ON Semiconductor®

http://onsemi.com

SOFT ULTRAFAST RECTIFIER 6.0 AMPERES, 200 VOLTS



CASE 369C



MARKING DIAGRAM



= Assembly Location

= Year

A

Υ

G

WW = Work Week

= Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
MSRD620CTG	DPAK (Pb-Free)	75 Units/Rail
MSRD620CTT4G	DPAK (Pb-Free)	2,500 / Tape & Reel
SSRD8620CTT4G	DPAK (Pb-Free)	2,500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MSRD620CTG, MSRD620CTT4G, SSRD8620CTT4G

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	200	V
Average Rectified Forward Current (At Rated V _R , T _C = 137°C) Per Leg Per Package	IO	3.0 6.0	A
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 20 kHz, T _C = 138°C) Per Leg	I _{FRM}	6.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions, Halfwave, Single Phase, 60 Hz) Per Package	I _{FSM}	50	A
Storage / Operating Case Temperature	T _{stg,} T _c	-55 to +175	°C
Operating Junction Temperature	TJ	-55 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Resistance – Junction-to-Case Per Leg	$R_{\theta JC}$	9.0	°C/W
Thermal Resistance – Junction-to-Ambient Per Leg	$R_{ hetaJA}$	80	°C/W

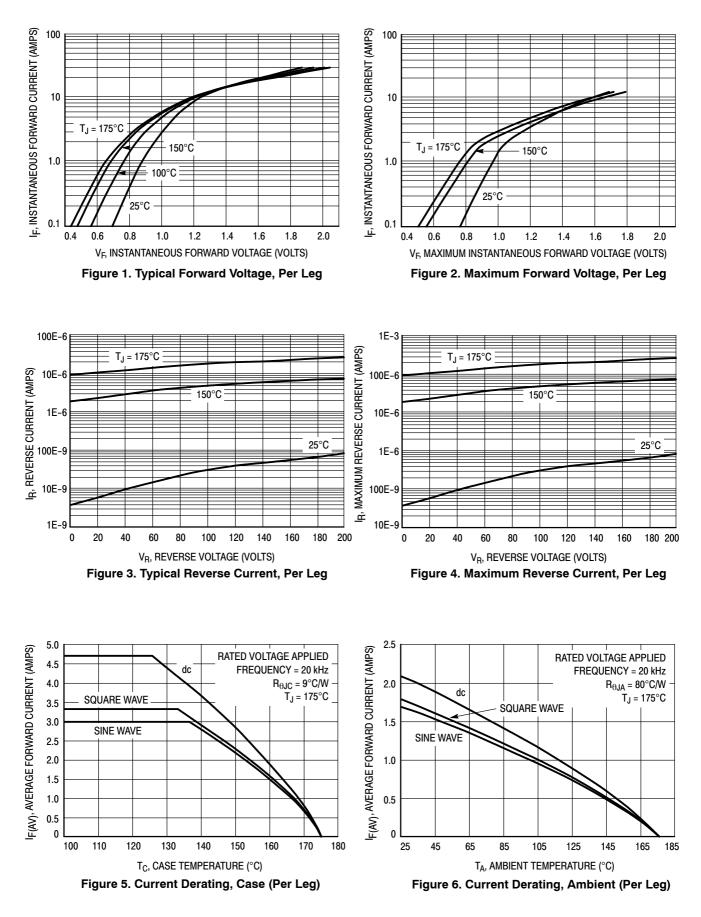
ELECTRICAL CHARACTERISTICS

Rating	Symbol	Va	lue	Unit
Maximum Instantaneous Forward Voltage (Note 1) (See Figure 2) Per Leg	V _F	T _J = 25°C	T _J = 150°C	V
(I _F = 3.0 A) (I _F = 6.0 A)		1.15 1.35	1.05 1.30	
Maximum Instantaneous Reverse Current (See Figure 4) Per Leg	I _R	T _J = 25°C	T _J = 150°C	μΑ
(V _R = 200 V) (V _R = 100 V)		5.0 2.0	200 100	
$\begin{array}{l} \mbox{Maximum Reverse Recovery Time (Note 2)} \\ \mbox{Per Leg} \\ (V_R = 30 \mbox{ V, I}_F = 1.0 \mbox{ A, di/dt} = 50 \mbox{ A/}\mu s) \\ (V_R = 30 \mbox{ V, I}_F = 3.0 \mbox{ A, di/dt} = 50 \mbox{ A/}\mu s) \end{array}$	t _{rr}		-5 55	ns
$\begin{array}{l} \mbox{Maximum Peak Reverse Recovery Current} \\ \mbox{Per Leg} \\ (V_R = 30 \mbox{ V}, I_F = 1.0 \mbox{ A}, di/dt = 50 \mbox{ A}/\mu s) \\ (V_R = 30 \mbox{ V}, I_F = 3.0 \mbox{ A}, di/dt = 50 \mbox{ A}/\mu s) \end{array}$	I _{RM}		.0 .0	A

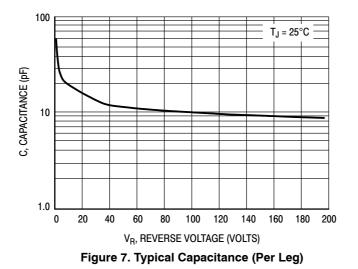
1. Pulse Test: Pulse Width \leq 250 µs, Duty Cycle \leq 2%.

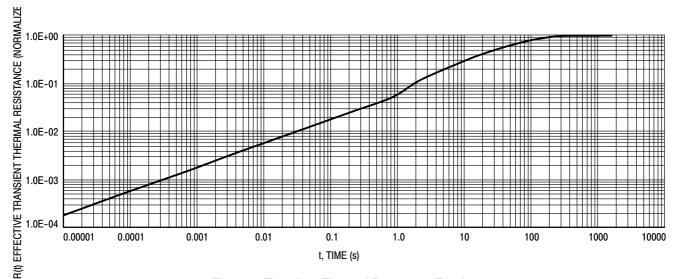
2. t_{rr} measured projecting from 25% of I_{RM} to ground.

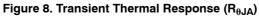
MSRD620CTG, MSRD620CTT4G, SSRD8620CTT4G

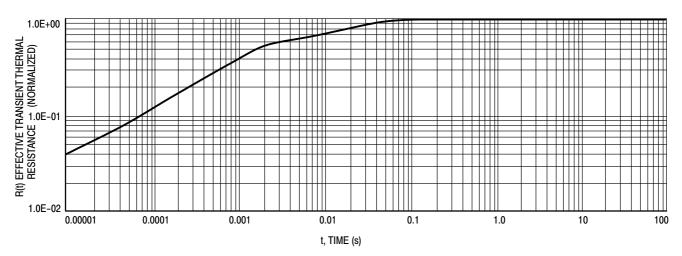


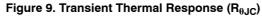
MSRD620CTG, MSRD620CTT4G, SSRD8620CTT4G





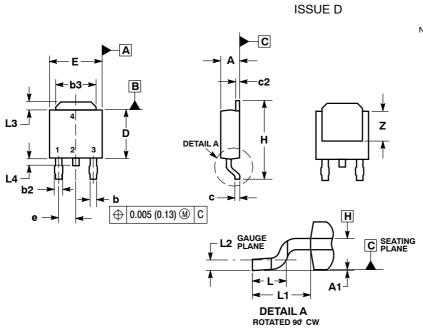






PACKAGE DIMENSIONS

DPAK (SINGLE GAUGE) CASE 369C-01

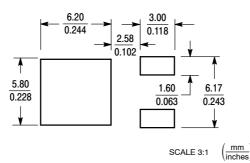


NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: INCHES. 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DI-
- THERMAL PAD CONTOUR OPTIONAL WITHIN DI-MENSIONS b3, L3 and Z.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE
- NOT EXCEED 0.006 INCHES PER SIDE. 5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 6. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.030	0.045	0.76	1.14
b3	0.180	0.215	4.57	5.46
С	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
E	0.250	0.265	6.35	6.73
е	0.090	BSC	2.29 BSC	
Н	0.370	0.410	9.40	10.41
L	0.055	0.070	1.40	1.78
L1	0.108 REF		2.74 REF	
L2	0.020 BSC		0.51 BSC	
L3	0.035	0.050	0.89	1.27
L4		0.040		1.01
Z	0.155		3.93	

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and use registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death agosciated with such unintended or unauthorized use patent shall claims and so for the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for seale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative