



N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
30	0.075 at V _{GS} = 10 V	3.6		
	0.115 at V _{GS} = 4.5 V	2.9		

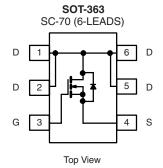
FEATURES

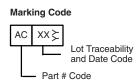
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- Thermally Enhanced SC-70 Package
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- Boost Converter in Portable Devices
 Low Gate Charge (3 nC)
- Low Current Synchronous Rectifier





Ordering Information: Si1426DH-T1-E3 (Lead (Pb)-free)

Si1426DH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 ^{\circ}\text{C}$, unle	ss otherwise r	oted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Comment /T 150 00\8	T _A = 25 °C	- I _D	3.6	2.8	_
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		2.6	2.1	
Pulsed Drain Current		I _{DM}	10		Α
Continuous Diode Current (Diode Conduction) ^a		I _S	1.3	0.8	
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	1.6	1.0	W
	T _A = 85 °C		0.8	0.5	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Martinian Institut to Australia	t ≤ 5 s	- R _{thJA}	60	80		
Maximum Junction-to-Ambient ^a	Steady State		100	125	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	34	45		

Note:

a. Surface mounted on 1" x 1" FR4 board.

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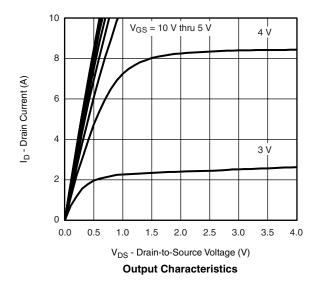
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.80		2.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS} -	V _{DS} = 24 V, V _{GS} = 0 V			1	μΑ	
		V _{DS} = 24 V, V _{GS} = 0 V, T _J = 85 °C			5		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	10			Α	
Drain-Source On-State Resistance ^a		$V_{GS} = 10 \text{ V}, I_D = 3.6 \text{ A}$		0.061	0.075		
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2.0 \text{ A}$		0.092	0.115	Ω	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 3.6 \text{ A}$		5		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 1.3 A, V _{GS} = 0 V		0.78	1.2	٧	
Dynamic ^b			•	•			
Total Gate Charge	Qg			1.9	3	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 3.6 \text{ A}$		0.75			
Gate-Drain Charge	Q_{gd}			0.75			
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r			12	18	ns	
Turn-Off Delay Time	t _{d(off)}			15	22		
Fall Time	t _f			9	15		
Source-Drain Reverse Recovery	t _{rr}	I _F = 1.4 A, dI/dt = 100 A/μs		40	70		

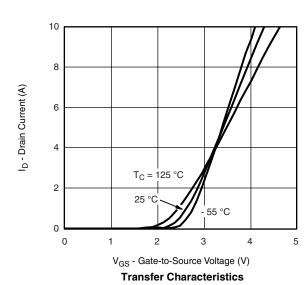
Notes:

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



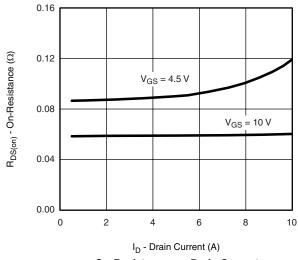




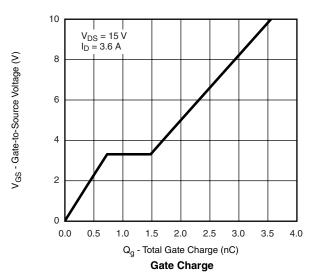




TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



On-Resistance vs. Drain Current



0.4

V_{SD} - Source-to-Drain Voltage (V)

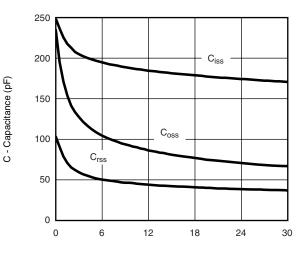
Source-Drain Diode Forward Voltage

0.6

0.8

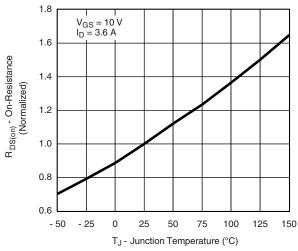
1.0

1.2

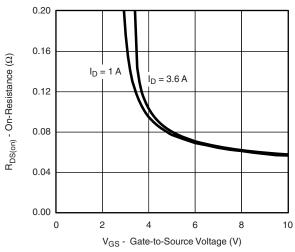


V_{DS} - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

0.0

0.2

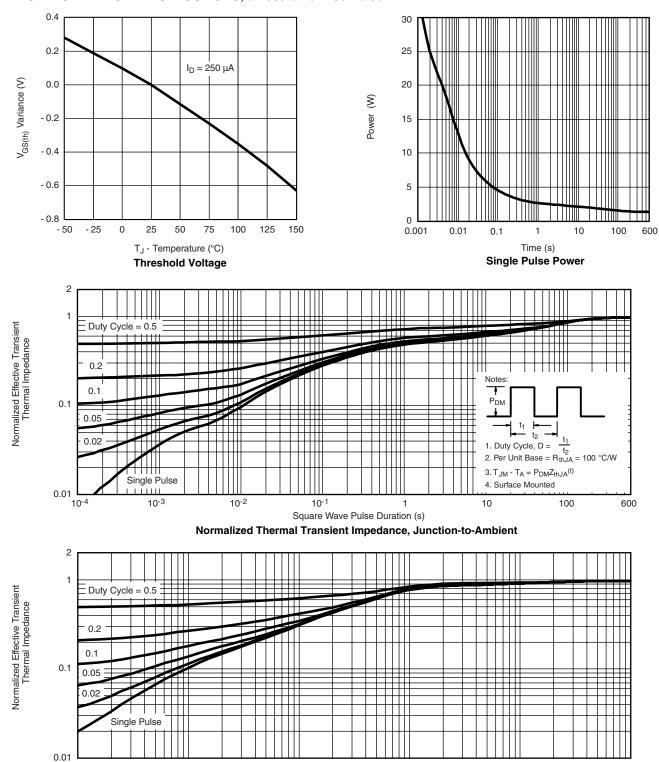
10

I_S - Source Current (A)

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Foot

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