

Vishay Siliconix

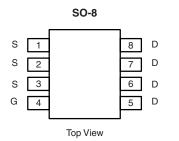
P-Channel 20-V (D-S) MOSFET

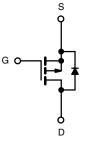
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
- 20	0.040 at V _{GS} = - 4.5 V	- 6.3			
	0.055 at V _{GS} = - 2.5 V	- 5.1			

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si9434BDY-T1-E3 (Lead (Pb)-free) Si9434BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 8			
	T _A = 25 °C	- I _D	- 6.3	- 4.5	•	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 5.0	- 3.6		
Pulsed Drain Current		I _{DM}	- 20		А	
Continuous Source Current (Diode Conduction) ^a		I _S	- 2.3	- 1.2		
	T _A = 25 °C	Р	2.5	1.3	W	
Maximum Power Dissipation ^a	T _A = 70 °C	P _D	1.6	0.8	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 10 s	- R _{thJA} R _{thJF}	45	50	°C/W
Maximum Junction-to-Ambient ^a	Steady State		80	95	
Maximum Junction-to-Foot (Drain)	Steady State		20	24	

Notes:

a. Surface Mounted on FR4 board, $t \leq$ 10 s.

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Typ. ^a	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.45		- 1.5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -20 V, V_{GS} = 0 V$			- 1	μΑ	
	I _{DSS}	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 70 °C			- 5		
On-State Drain Current ^a	1	$V_{DS} \leq$ - 5 V, V_{GS} = - 4.5 V $-$				^	
	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 2.5 V	- 5			A	
Drain-Source On-State Resistance ^b	D	V _{GS} = - 4.5 V, I _D = - 6.3 A		0.033	0.040	0	
	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 5.1 A		0.044	0.055	Ω	
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 9 V, I _D = - 6.3 A		10		S	
Diode Forward Voltage ^b	V _{SD}	I _S = - 2.3 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^a							
Total Gate Charge	Qg			12	18		
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_{D} = - 6.3 A		1.7		nC	
Gate-Drain Charge	Q _{gd}			3.5			
Gate Resistance	Rg			7		Ω	
Turn-On Delay Time	t _{d(on)}			15	25		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		45	75	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		80	130		
Fall Time	t _f			60	100		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.3 A, dl/dt = 100 A/μs		40	70		

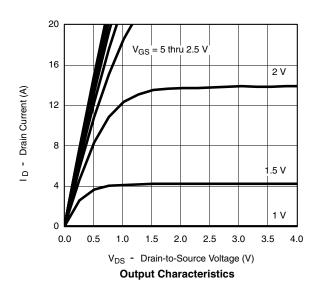
Notes:

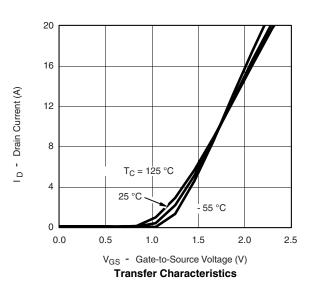
a. For design aid only; not subject to production testing.

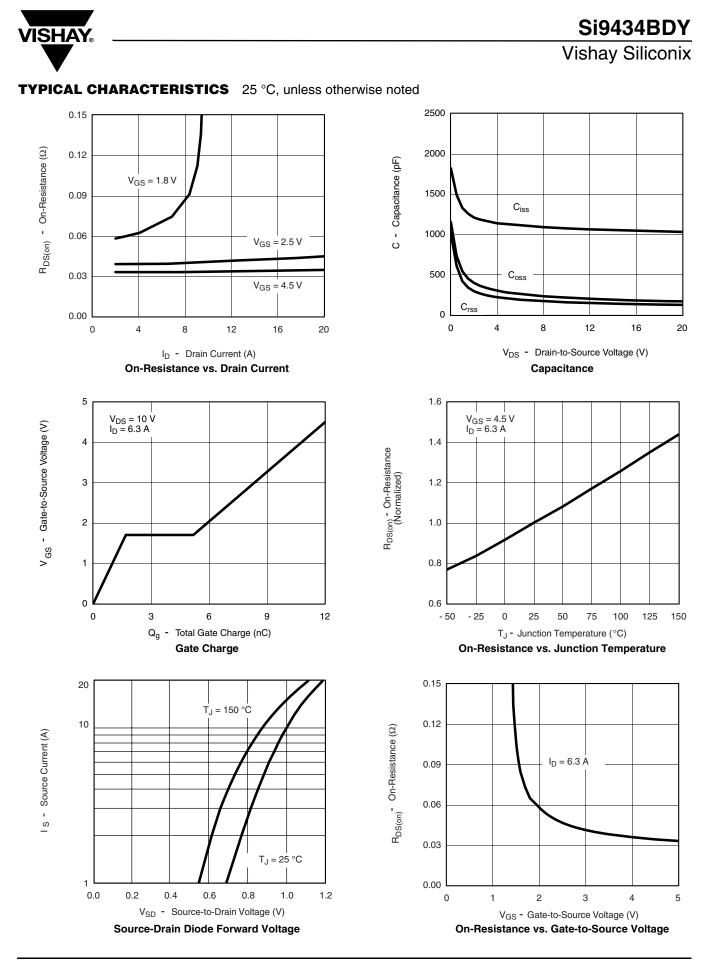
b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





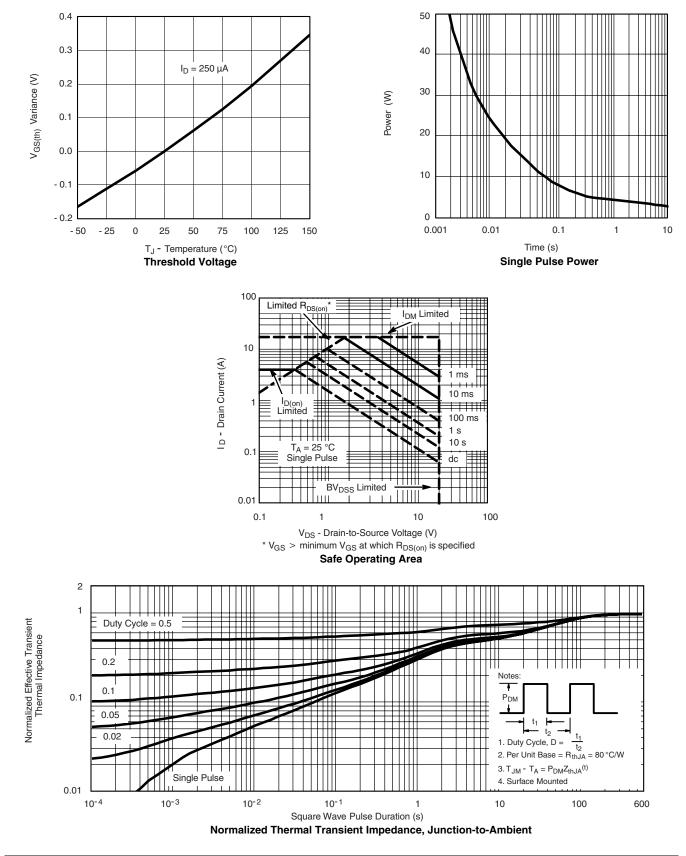


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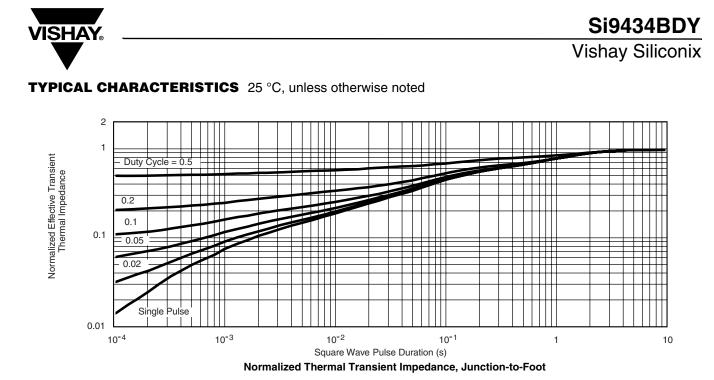
Si9434BDY

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