CMPTH81

SURFACE MOUNT PNP SILICON RF TRANSISTOR



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPTH81 type is a PNP Silicon RF Transistor, epoxy molded in a surface mount package, designed for general RF amplifier applications.

MARKING CODE: C3D or 3D



MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	20	V
Collector-Emitter Voltage	V_{CEO}	20	V
Emitter-Base Voltage	V_{EBO}	3.0	V
Continuous Collector Current	l _C	50	mA
Power Dissipation (Note 1)	P_{D}	225	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-55 to +150	°C
Thermal Resistance	$\Theta_{\sf JA}$	556	°C/W

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =10V		100	nA
I _{EBO}	V _{EB} =2.0V		100	nA
BVCBO	I _C =10μA	20		V
BV CEO	I _C =1.0mA	20		V
BVEBO	I _E =10μA	3.0		V
VCE(SAT)	I _C =5.0mA, I _B =500μA		0.5	V
V _{BE(ON)}	V_{CE} =10V, I_{C} =5.0mA		0.9	V
hFE	V_{CE} =10V, I_{C} =5.0mA	60		
f_{T}	V_{CE} =10V, I_{C} =5.0mA, f=100MHz	600		MHz
C_{cb}	V_{CB} =10V, I_E =0, f=1.0MHz		0.85	pF
c_{ce}	V_{CB} =10V, I_B =0, f=1.0MHz		0.65	pF

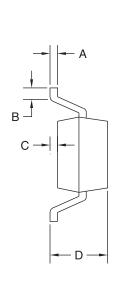
Notes: (1) FR-4 Epoxy PCB Substrate 1.6" x 1.6" x 0.06"

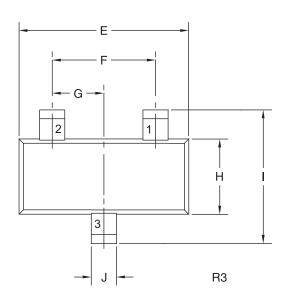
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SOT-23 CASE - MECHANICAL OUTLINE





LEAD CODE:

- 1) Base 2) Emitter 3) Collector

MARKING CODE: C3D or 3D

DIMENSIONS								
	INCHES		MILLIMETERS					
SYMBOL	MIN	MAX	MIN	MAX				
Α	0.003	0.007	0.08	0.18				
В	0.006	-	0.15	-				
С	-	0.005	-	0.13				
D	0.035	0.043	0.89	1.09				
Е	0.110	0.120	2.80	3.05				
F	0.075		1.90					
G	0.037		0.95					
Н	0.047	0.055	1.19	1.40				
ĺ	0.083	0.098	2.10	2.49				
J	0.014	0.020	0.35	0.50				
0.07.00 (DE) (D0)								

SOT-23 (REV: R3)

R5 (15-August 2012)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- · PbSn plating options
- Package details
- · Application notes
- · Application and design sample kits
- · Custom product and package development

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA Main Tel: (631) 435-1110

Main Fax: (631) 435-1824 Support Team Fax: (631) 435-3388

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