

# SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

# SCH1433-

# N-Channel Silicon MOSFET General-Purpose Switching Device Applications

# Features

- 1.8V drive
- Halogen free compliance
- Protection diode in

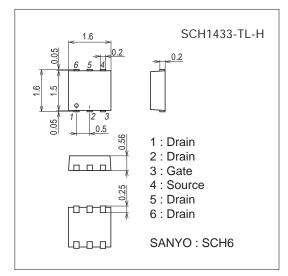
## Specifications

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	۱ <sub>D</sub>		3.5	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	14	А
Allowable Power Dissipation	PD	When mounted on ceramic substrate (900mm <sup>2</sup> x0.8mm)	0.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Package Dimensions

unit : mm (typ) 7028-002



#### Product & Package Information

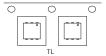
- Package
- JEITA, JEDEC
- Minimum Packing Quantity : 5,000 pcs./reel

#### Packing Type : TL

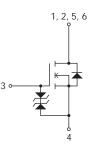


: SCH6

: SOT-563





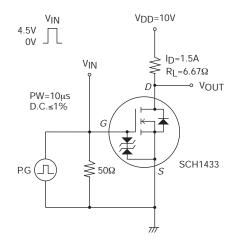


#### SANYO Semiconductor Co., Ltd. http://semicon.sanyo.com/en/network

<b>Electrical Cha</b>	racteristics at Ta=25°C
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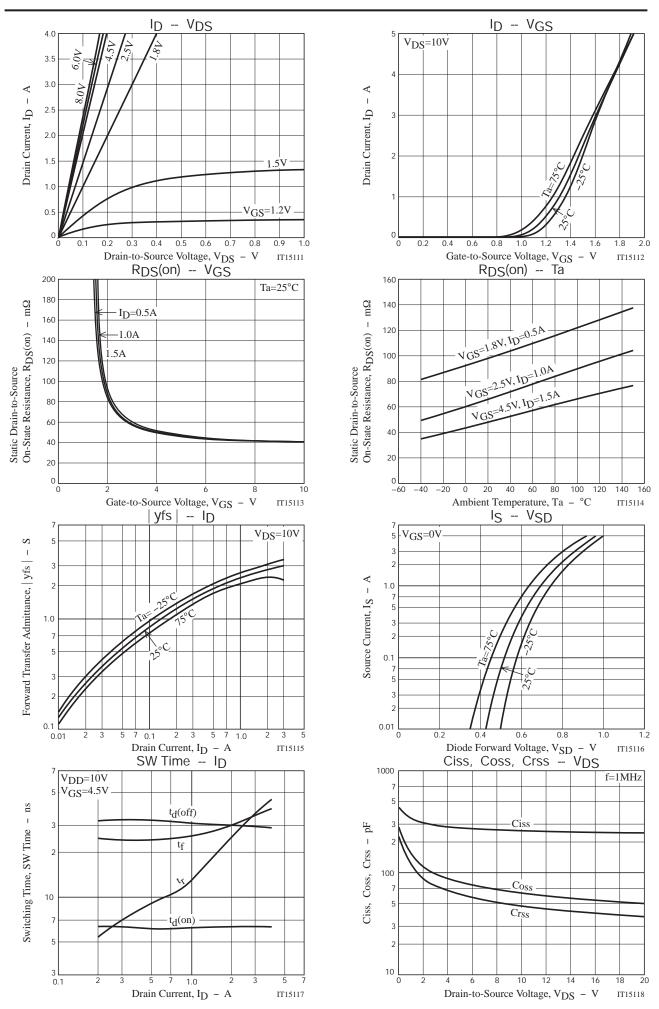
Decemeter	Symbol	Constituione.	Ratings			11-14
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	20			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V			1	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA 0.4			1.3	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =1.5A	1.68	2.8		S
	R <sub>DS</sub> (on)1	ID=1.5A, VGS=4.5V		49	64	mΩ
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)2	ID=1A, VGS=2.5V		68	95	mΩ
	R <sub>DS</sub> (on)3	ID=0.5A, VGS=1.8V		99	149	mΩ
Input Capacitance	Ciss			260		рF
Output Capacitance	Coss	VDS=10V, f=1MHz		65		рF
Reverse Transfer Capacitance	Crss	-		50		рF
Turn-ON Delay Time	t <sub>d</sub> (on)			6.2		ns
Rise Time	tr			19		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		30		ns
Fall Time	tf			28		ns
Total Gate Charge	Qg			2.8		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =3.5A		0.6		nC
Gate-to-Drain "Miller" Charge	Qgd	1		0.9		nC
Diode Forward Voltage	V <sub>SD</sub>	IS=3.5A, VGS=0V		0.85	1.2	V

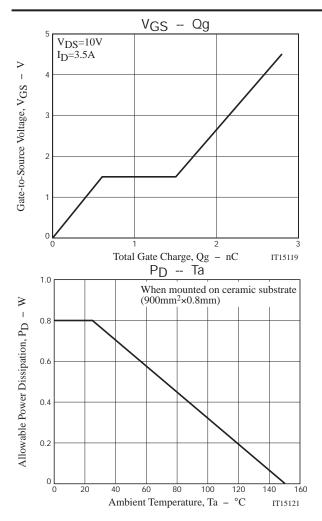
## Switching Time Test Circuit

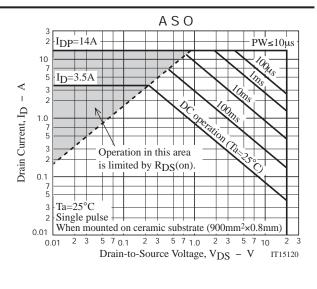


#### **Ordering Information**

Device	Device Package		memo		
SCH1433-TL-H	SCH6	5,000pcs./reel	Pb Free and Halogen Free		







### Taping Specification SCH1433-TL-H

1. Packing Format

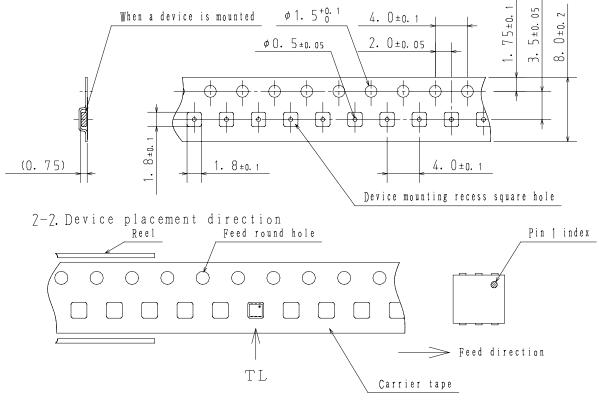
Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format			
	Туре	Reel	Inner box	Outer box	Inner BOX (C-1)			Outer BOX (A-7)
SCH6	SCH6	5,000	25,000	150,000	5 reels contained			6 inner boxes contained
					Dimensions:mm (external)			Dimensions:mm (external)
					18	3×72×	185	440×195×210
	<u>Reel label, Inner box label</u> <u>Outer box label</u>							
Packing met	<u>Packing method</u> (unit:mm) It is a label at the time of factory shipments. The form of a label may change in physical distribution process.							m of a label may change in physical
°	$\geq$		<	6	59	>	<	108
	Type LOT			) TYPE 0000 				TYPE CODE IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII
	Quan			0 QTY 0, 00 	0 (1)        Lea	D FREE # ~7		QTY 0.000 PCS LEAD FREE #   LOT COCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
	Orig	in	>A:	* Z 0 7 2 2 0 SSEMBLY:**** (	005310 DIFFUSION	C* [:****)		
NOTE (1)								
<u>Reel label</u> The LEAD FREE ₩ description shows that the surface treatment of the terminal is lead free.								
				Label		JEITA	Phase	
				LEAD FRE	EE 3	JEITA F	hase 3A	

LEAD FREE 4

JEITA Phase 3

2. Taping configuration

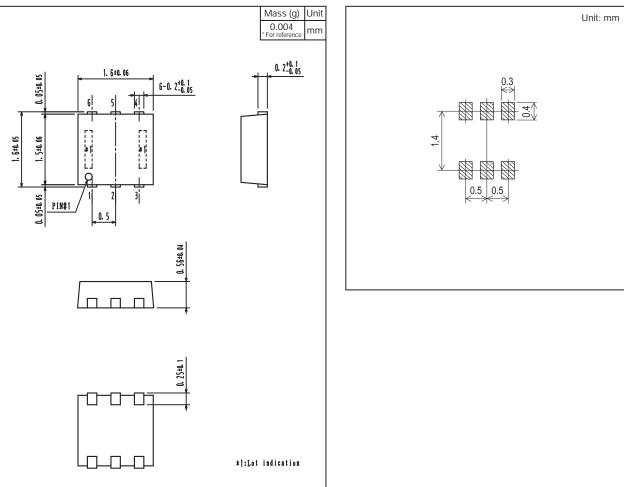
2-1. Carrier tape size (unit:mm)



Those with pin 1 index on the feed hole side  $\cdots \cdots TL$ 

Land Pattern Example

## Outline Drawing SCH1433-TL-H



Note on usage : Since the SCH1433 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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